

Refine Search

Search Results -

Term	Documents
BRIDGING	63136
BRIDGINGS	80
(70 AND BRIDGING).USPT,JPAB.	0
(L70 AND BRIDGING).USPT,JPAB.	0

Database:

- US Pre-Grant Publication Full-Text Database
- US Patents Full-Text Database**
- US OCR Full-Text Database
- EPO Abstracts Database
- JPO Abstracts Database
- Derwent World Patents Index
- IBM Technical Disclosure Bulletins

Search:

Refine Search

Recall Text

Clear

Interrupt

Search History

DATE: Friday, June 24, 2005 [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

DB=USPT,JPAB; PLUR=YES; OP=ADJ

L73 L70 and bridging

L72 L70 and bridge

DB=USPT; PLUR=YES; OP=ADJ

L71 L70 and plurality adj channels

L70 L69 and channel

L69 L68 and virtual

L68 port and bell adj atlantic and 1394 and interface

L67 L66 and VCI

L66 L64 and isochronous

L65 L64 and loop-free

L64 L62 and channels

Hit Count Set Name

result set

0 L73

0 L72

0 L71

8 L70

8 L69

9 L68

0 L67

13 L66

0 L65

59 L64

<u>L63</u>	L62 and plurality adj channels	1	<u>L63</u>
<u>L62</u>	port and bridge and 1394 and data adj interface	74	<u>L62</u>
<u>L61</u>	portand bell adj atlantic and 1394 and interface	0	<u>L61</u>
<u>L60</u>	portal and bell adj atlantic and 1394 and interface	0	<u>L60</u>
<u>L59</u>	L58 and isochronous	1	<u>L59</u>
<u>L58</u>	L57 and 1394	2	<u>L58</u>
<u>L57</u>	L56 and virtual	64	<u>L57</u>
<u>L56</u>	portal and AT&T	114	<u>L56</u>
<u>L55</u>	L53 and channels	3	<u>L55</u>
<u>L54</u>	L53 and channels and extends	1	<u>L54</u>
<u>L53</u>	L52 and virtual	3	<u>L53</u>
<u>L52</u>	1394 near bus and AT&T and port and isochronous	3	<u>L52</u>
<u>L51</u>	L50 and channels	0	<u>L51</u>
<u>L50</u>	L49 and port and interface	1	<u>L50</u>
<u>L49</u>	1394 adj bus and loop-free	1	<u>L49</u>
<u>L48</u>	L47 and virtual	0	<u>L48</u>
<u>L47</u>	L46 and port	1	<u>L47</u>
<u>L46</u>	channel adj extends near interface	4	<u>L46</u>
<u>L45</u>	channel adj extends near port	18	<u>L45</u>
<u>L44</u>	L43 and port	1	<u>L44</u>
<u>L43</u>	L38 and 1394	1	<u>L43</u>
<u>L42</u>	L41 and 1394	1	<u>L42</u>
<u>L41</u>	L40 and VCI	1	<u>L41</u>
<u>L40</u>	L39 and virtual	1	<u>L40</u>
<u>L39</u>	L37 and port and bus	1	<u>L39</u>
<u>L38</u>	L3 and 1394 near bus	1	<u>L38</u>
<u>L37</u>	L2 and 1394 near bus	1	<u>L37</u>
<u>L36</u>	L35 and channels and port	1	<u>L36</u>
<u>L35</u>	channel near extends near interface	4	<u>L35</u>
<u>L34</u>	port near interfacing near bus and channels	1	<u>L34</u>
<u>L33</u>	L32 and virtual	0	<u>L33</u>
<u>L32</u>	L31 and channels	1	<u>L32</u>
<u>L31</u>	L30 and interface	2	<u>L31</u>
<u>L30</u>	L29 and port	2	<u>L30</u>
<u>L29</u>	L28 and bus	2	<u>L29</u>
<u>L28</u>	L1 and IEEE1394	2	<u>L28</u>
<u>L27</u>	L1 and 1394 adj bus	6	<u>L27</u>
<u>L26</u>	L25 and virtual	1	<u>L26</u>
<u>L25</u>	L22 and channels	2	<u>L25</u>
<u>L24</u>	L22 and plurality near channel	0	<u>L24</u>
<u>L23</u>	L22 and plurality adj channels	0	<u>L23</u>

<u>L22</u>	L21 and port	4	<u>L22</u>
<u>L21</u>	L1 and 1394 near bus	6	<u>L21</u>
<u>L20</u>	L18 and VPI	0	<u>L20</u>
<u>L19</u>	L18 and VCI	0	<u>L19</u>
<u>L18</u>	L17 and port and bus	5	<u>L18</u>
<u>L17</u>	L3 and plurality adj channels	15	<u>L17</u>
<u>L16</u>	L14 and VPI	0	<u>L16</u>
<u>L15</u>	L14 and VCI	0	<u>L15</u>
<u>L14</u>	L13 and port	3	<u>L14</u>
<u>L13</u>	L11 and bus	5	<u>L13</u>
<u>L12</u>	L11 and data adj bus	3	<u>L12</u>
<u>L11</u>	l2 and plurality adj channels	17	<u>L11</u>
<u>L10</u>	L9 and VPI and VCI	1	<u>L10</u>
<u>L9</u>	L6 and port	20	<u>L9</u>
<u>L8</u>	L7 and port	0	<u>L8</u>
<u>L7</u>	L6 and VPI/VCI	1	<u>L7</u>
<u>L6</u>	L1 and plurality adj channels	34	<u>L6</u>
<u>L5</u>	L4 and plurality adj channels	0	<u>L5</u>
<u>L4</u>	L1 and port near data adj bus	2	<u>L4</u>
<u>L3</u>	370/487.ccls.	161	<u>L3</u>
<u>L2</u>	370/486.ccls.	185	<u>L2</u>
<u>L1</u>	370/463.ccls.	583	<u>L1</u>

END OF SEARCH HISTORY